A. Given the following instruction sequence, what is the range of feasible addresses for the label *target* on the MIPS architecture?

```
start:    beq$1, $2, target
          add$4, $5, $6
.
.
.
target:   sub$6, $4, $8
```

B. What is the difference between the *j* and *jal* instructions?

C. Show a sequence of SPIM instructions that will branch to the label loop if the contents of register $t0 is 13.

D. The instruction BLT (Branch on less than or equal too) is not a native instruction. We could implement a pseudo instruction that the assembler could recognize and translate into sequence of SPIM instructions. Show the SPIM code that could be used to implement this test.

ble $t2, $t3, loop

