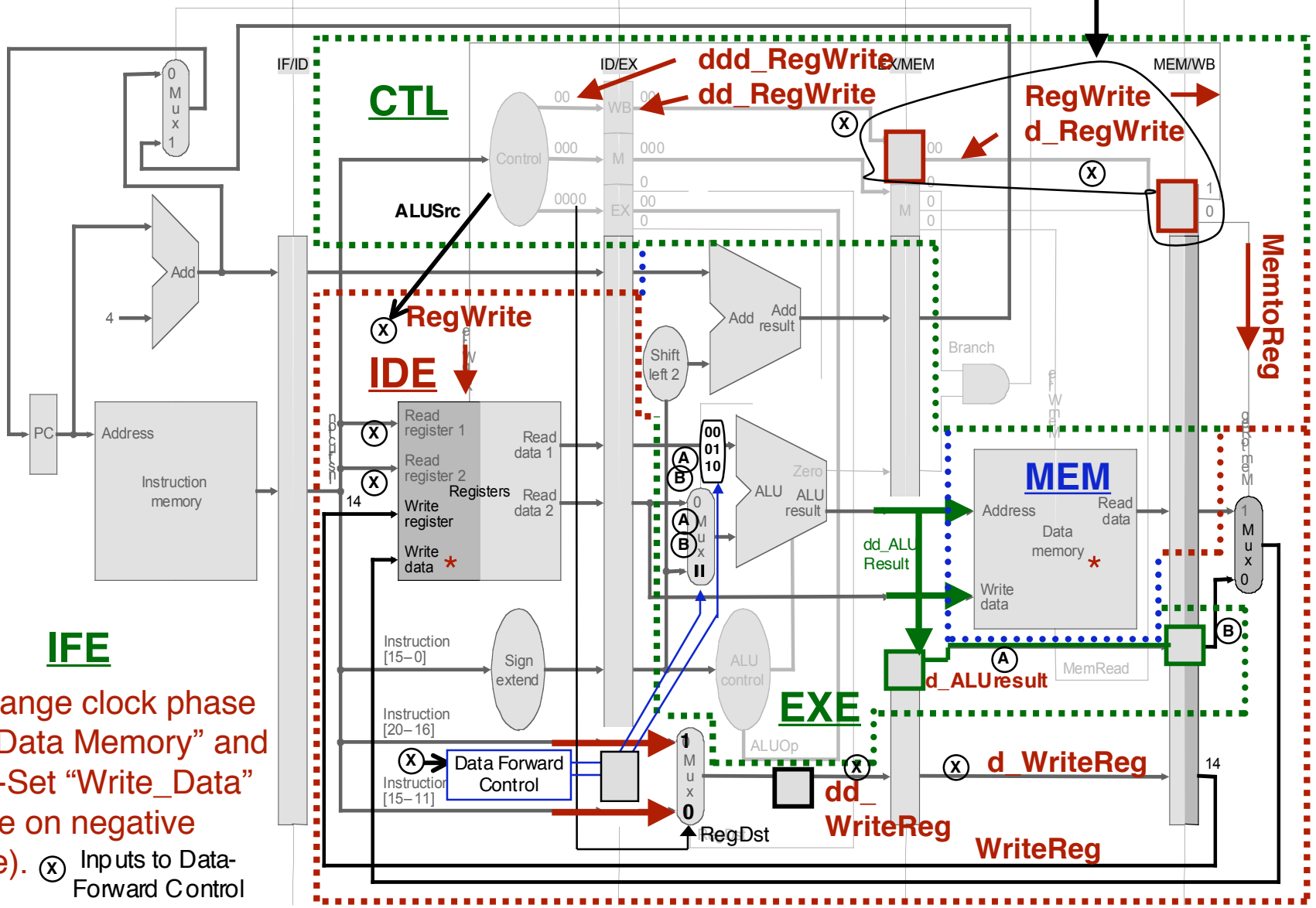
Move RegWrite to IDE

MIPS modules for Lab-4, Data Forwarding



* Change clock phase for “Data Memory” and Reg-Set “Write_Data” (write on negative edge). (X) Inputs to Data-Forward Control